

## **Small Surface Mount Ultrafast Diodes**

#### **Features**

- · For surface mounted applications
- Low profile package
- · Ideal for automated placement
- · Glass passivated
- High temperature soldering: 260 °C/ 10 seconds at terminals
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### **Mechanical Data**

Case: JEDEC DO-219AB (SMF®) Plastic case

Polarity: Band denotes cathode end

Weight: approx. 15 mg







17249

#### Packaging codes-options:

GS18 / 10 K per 13" reel (8 mm tape), 50 k/box GS08 / 3 K per 7" reel (8 mm tape), 30 k/box

#### **Parts Table**

| Part  | Ordering code            | Marking | Remarks       |
|-------|--------------------------|---------|---------------|
| ES07B | ES07B-GS18 or ES07B-GS08 | ЕВ      | Tape and Reel |
| ES07D | ES07D-GS18 or ES07D-GS08 | ED      | Tape and Reel |

## **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

| Parameter   | Test condition                       | Part  | Symbol             | Value | Unit |  |
|---|--------------------------------------|-------|--------------------|-------|------|--|
| Maximum repetitive peak reverse voltage                 |                                      | ES07B | V <sub>RRM</sub>   | 100   | V    |  |
|   |                                      | ES07D | V <sub>RRM</sub>   | 200   | V    |  |
| Maximum RMS voltage                                     |                                      | ES07B | V <sub>RMS</sub>   | 70    | V    |  |
|   |                                      | ES07D | V <sub>RMS</sub>   | 140   | V    |  |
| Maximum DC blocking voltage                             |                                      | ES07B | V <sub>DC</sub>    | 100   | V    |  |
|   |                                      | ES07D | V <sub>DC</sub>    | 200   | V    |  |
| Maximum average forward rectified current               | T <sub>tp</sub> = 105 °C             |       | I <sub>F(AV)</sub> | 1.2   | Α    |  |
|   | T <sub>A</sub> = 65 °C <sup>1)</sup> |       | I <sub>F(AV)</sub> | 0.5   | Α    |  |
| Peak forward surge current 8.3 ms single half sine-wave | T <sub>L</sub> = 25 °C               |       | I <sub>FSM</sub>   | 30    | А    |  |

<sup>1)</sup> Mounted on epoxy glass PCB with 3 x 3 mm, Cu pads ( $\geq$  40  $\mu$ m thick)

#### **Thermal Characteristics**

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 $T_{amb}$  = 25 °C, unless otherwise specified

| Parameter  | Test condition | Symbol                            | Value         | Unit |
|--|----------------|-----------------------------------|---------------|------|
| Thermal resistance junction to ambient air <sup>1)</sup> |                | $R_{thJA}$                        | 180           | K/W  |
| Operating junction and storage temperature range         |                | T <sub>J</sub> , T <sub>STG</sub> | - 55 to + 150 | °C   |

<sup>1)</sup> Mounted on epoxy glass PCB with 3 x 3 mm, Cu pads (≥ 40 µm thick)

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#### **Electrical Characteristics**

 $T_{amb}$  = 25 °C, unless otherwise specified

| Parameter   | Test condition  | Symbol          | Min | Тур. | Max  | Unit |
|---|---|-----------------|-----|------|------|------|
| Maximum instantaneous forward voltage                   | 1.0 A <sup>3)</sup>   | V <sub>F</sub>  |     |      | 0.98 | V    |
| Maximum DC reverse current at rated DC blocking voltage | T <sub>A</sub> = 25°C   | I <sub>R</sub>  |     |      | 10   | μА   |
|   | T <sub>A</sub> = 100°C  | I <sub>R</sub>  |     |      | 50   | μА   |
| Reverse recovery time                                   | $I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$ | t <sub>rr</sub> |     |      | 25   | ns   |
| Typical capacitance                                     | 4 V, 1 MHz  | C <sub>j</sub>  |     | 4    |      | pF   |

 $<sup>^{3)}</sup>$  Pulse test, 300  $\mu s$  pulse with 1 % duty cycle

# Typical Characteristics (Tamb = 25 °C unless otherwise specified)

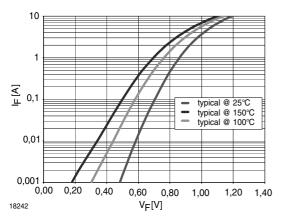


Figure 1. Typical Forward Characteristics

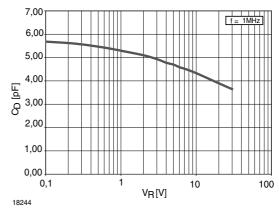


Figure 3. Typ. Diode Capacitance vs. Reverse Voltage

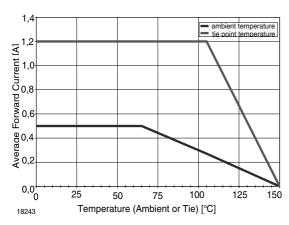


Figure 2. Forward Current Derating Curve

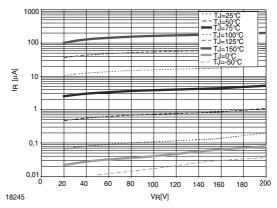
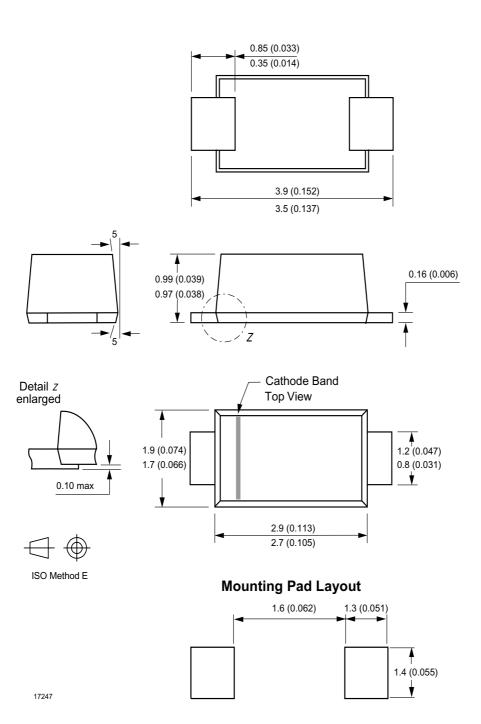


Figure 4. Typical Reverse Characteristics

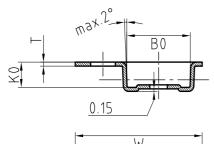


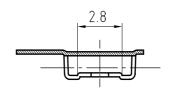
# **Package Dimensions in mm (Inches)**

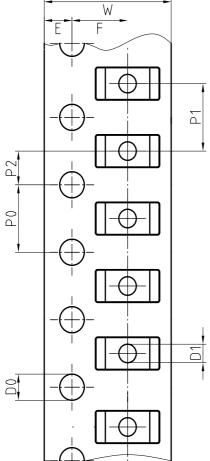


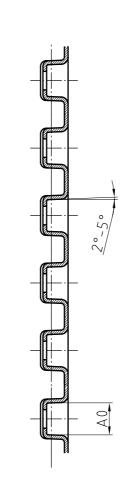
# **Blistertape for SMF**











| Mat: | Α0  | В0  | K0  | W   | T     | P0  | P2  | P1  | D0  | D1 | Е    | F   |
|------|-----|-----|-----|-----|-------|-----|-----|-----|-----|----|------|-----|
| PS   | 1.9 | 4.0 | 1.5 | 8.0 | 0.235 | 4.0 | 2.0 | 4.0 | 1.5 | 1  | 1.75 | 3.5 |

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### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect t their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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